

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		10/639,942	ROBERT H. DENNARD	
Examiner		Art Unit		Page 1 of 1
Stanetta D. Isaac		2812		

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,342,717	01-2002	Komatsu, Hiroshi	257/347
	B	US-5,273,921	12-1993	Neudeck et al.	438/157
	C	US-5,929,479	07-1999	Oyama, Kenichi	257/315
	D	US-6,541,345	04-2003	Komatsu, Hiroshi	438/311
	E	US-6,496,034	12-2002	Forbes et al.	326/41
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Stanley Wolf and Richard N. Tauber, Silicon Processing For The VLSI Era, 1986, Lattice Press, Vol. I, pages 303-308
	V	Stanley Wolf, Silicon Processing For The VLSI Era, 1990, Lattice Press, Vol. II, pages 45-47 and 144-146
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.